



AF/2876

<b>AMENDMENT TRANSMITTAL LETTER</b>			Docket No. SON-1976
Application No. 09/739,318	Filing Date December 19, 2000	Examiner E. Labaze	Art Unit 2876

Applicant(s): Yuji MURAYAMA, et al

Invention: PORTABLE ELECTRONIC APPARATUS, IC CARD AND READER/WRITER

**TO THE COMMISSIONER FOR PATENTS**

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	25	- 20 =	5	x 18.00	90.00
Independent Claims	6	- 6 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:</b>					90.00

- ☒ Large Entity ☐ Small Entity
- ☐ No additional fee is required for this amendment.
- ☒ Please charge Deposit Account No. 18-0013 in the amount of \$ 90.00.  
A duplicate copy of this sheet is enclosed.
- ☐ A check in the amount of \$ \_\_\_\_\_ to cover the filing fee is enclosed.
- ☐ Payment by credit card. Form PTO-2038 is attached.
- ☒ The Commissioner is hereby authorized to charge and credit Deposit Account No. 18-0013 as described below. A duplicate copy of this sheet is enclosed.
- ☒ Credit any overpayment.
- ☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

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Dated: January 7 2003

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Patent Application of

Yuji MURAYAMA et al.

Art Unit: 2876

Serial No. 09/739,318

Examiner: E. Labaze

Filed: December 19, 2000

For: PORTABLE ELECTRONIC APPARATUS, IC CARD AND READER/WRITER

REQUEST FOR RECONSIDERATION

BOX AF

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Final Office Action mailed on November 7, 2002, please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows.

13. A portable electronic apparatus comprising:  
a clock generation circuit, said clock generation circuit using a  
received signal to generate a clock signal and a sampling  
signal,

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